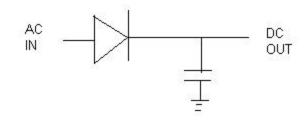
How to use S-parameter data files in Libra

Ray Woodward

This tells how to design a diode detector circuit to convert AC to DC voltage



Read through the whole tutorial before you start. You will go through two sections: Simulation and Layout

Simulation

- Use this (as you have in previous tutorials) to simulate the RF performance of your circuit. You need to include the lengths of all lines (including the pads for soldering elements to). These are found in the layout section, so you will do some back-and-forth between layout and simulation.
- Some elements are available in the standard libraries (the cap in this lab), others are not (the diode in this lab). When they are not available, you will have to "create" them using user-defined S-parameters and lines (MLIN). These can come from data sheets (like the HP website) or from measurements (like for this lab).

Layout:

- "Artwork" is used to define the size and shape of solder connects for components like capacitors, diodes, RF chips, etc.
- The dimensions of solder pads (where you actually connect your circuit) are specified on data sheets. They are also available in the libra "library" for many standard elements.
- The size of pads add length to your lines, so you will need to include them in your simulation for most accurate results. This means you need to figure out their size before you simulate the circuit, so you will do some back-and-forth between layout and simulation.

Simulation:

First you need the S-parameters of your diode....

There are two ways to get your Sparameters:

- Measure them (most accurate)
- Find them on the web or other data sheet (we'd like this to be most accurate, but for this lab it isn't)
- These methods are described below, for your reference. Measured values are given at the end.

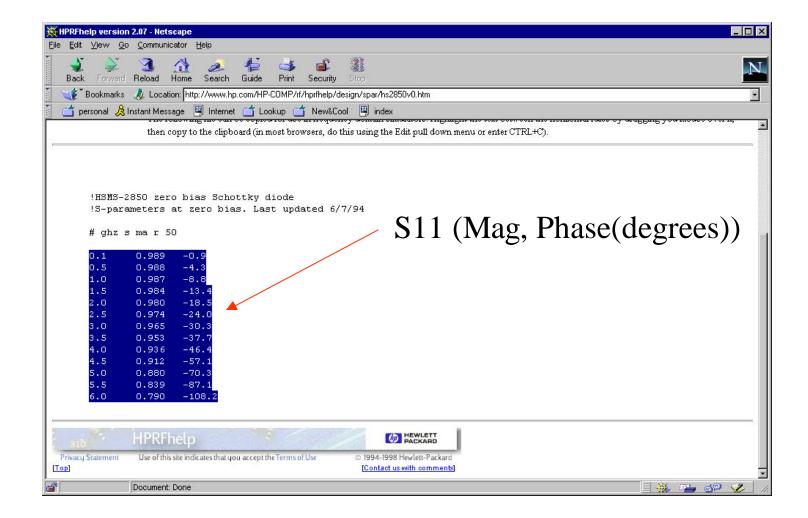
To measure S-parameters of the diode

- Build the circuit with just a length of line attached to the device.
- Measure the input impedance using the network analyzer
- Compensate for the length of the line to find the S-parameters of your device (matlab code to do this is included)

OR: To find the S parameters of your HP diode, Look on this web page:http://www.hp.com/HP-COMP/rf/hprfhelp/products/diodes/hsms285x.htm#s-data

HPRFhelp version 2.07 - Netscape				
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	PT (XTI)		2	
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Demonstration	Circuit Boards			-1
Document: Done				

Copy and Paste the data into a text file (diode.txt). Put this text file into your project directory.



You may have measured or acquired only the S11 values of a diode.

Find the other S-parameters (such as with the included matlab code), and create a text file that has 2-port S-parameters as shown on the next slide.

- S11 = reflection coefficient
- S21 = 1 + S11
- S22 = reflection coefficient (1+j0 for a diode)
- S12 = 1 + S22 (or in this case, for a diode, it is zero)

Measured 2-port S-parameters of your diode *including* the pads: filename is diode2pt.txt

BEGIN ACDATA

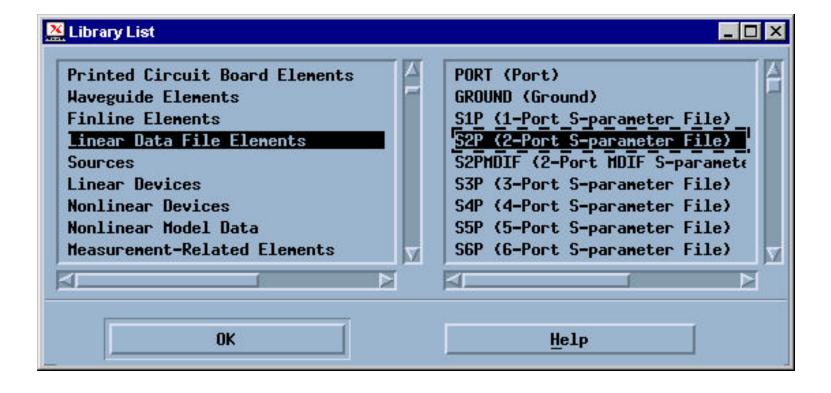
#	GHz	S	MA	R 50.0				
!% F	n11x n1	1y n21x n21y	[.] n12x n12	y n22x n22y				
1.75	0.9683	-23.8255	1.9259	-11.7179	0	0	1.0000	0
2.0	0.9683	-29.8098	1.9021	-14.6593	0	0	1.0000	0
2.4	0.9521	-35.1478	1.8610	-17.1282	0	0	1.0000	0
2.6	0.9529	-35.7572	1.8586	-17.4327	0	0	1.0000	0
3.0	0.8956	-44.0234	1.7578	-20.7357	0	0	1.0000	0
! END								

Copy diode2pt.txt to the data folder in your current project directory.

Click on the Element Library

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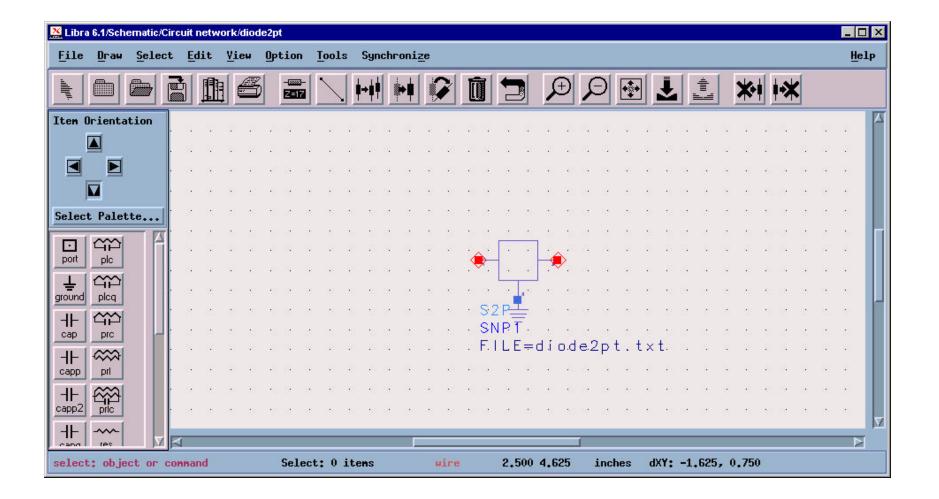
Choose Linear Data File, 2-Port S-parameters



Insert Filename

🔀 Item Parameters	
<pre>k item Parameters 2-Port S-parameter File * S2P ID SNP1 Parameters FILE= TEMP=*</pre>	Value type S 2-port file Value Value Value Options
FILE:Data File Name	
OK Apply Can	cel Reset <u>H</u> elp

You will get a 3-port picture of a diode. Add a ground to the extra port on the bottom.



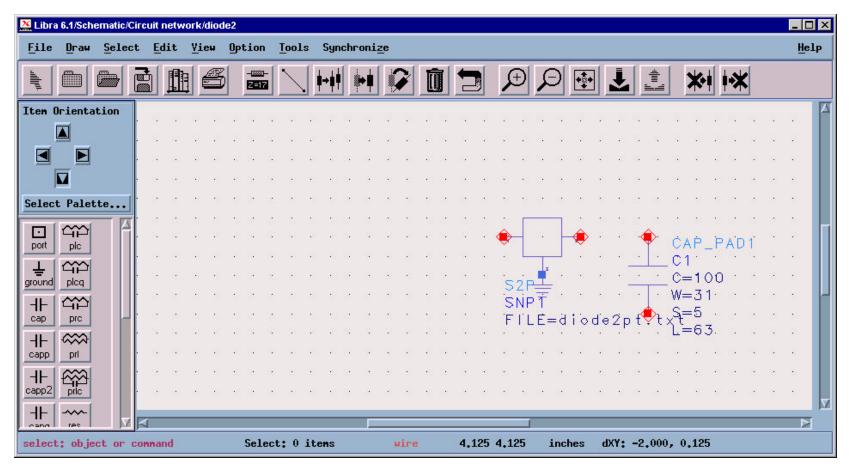
Now add a capacitor pad for soldering. Cap_pad1 represents a capacitor WITH its solder pads. Use the library again:

🔀 Library List	_ _ ×
LineCalc Elements E-Syn Elements Spice Elements Lumped Elements for EMS Lumped Items (with artwork) Linear Data File Items (with artwork) PG-able Block Characters Alignment and Outline Artwork Package Artwork	PORT (Port) GROUND (Ground) CAP_CONN (Capacitor (conn artwo CAP_SPAC (Capacitor (pad artwo) CAP_SPAC (Capacitor (space artwo) CAPQ_CONN (Capacitor With Q (co CAPQ_PAD1 (Capacitor With Q (pa CAPQ_SPAC (Capacitor With Q (sp CAPQ_SPAC (Capacitor With Q (sp CAPP_CONN (Chip Capacitor (obso
ОК	Help

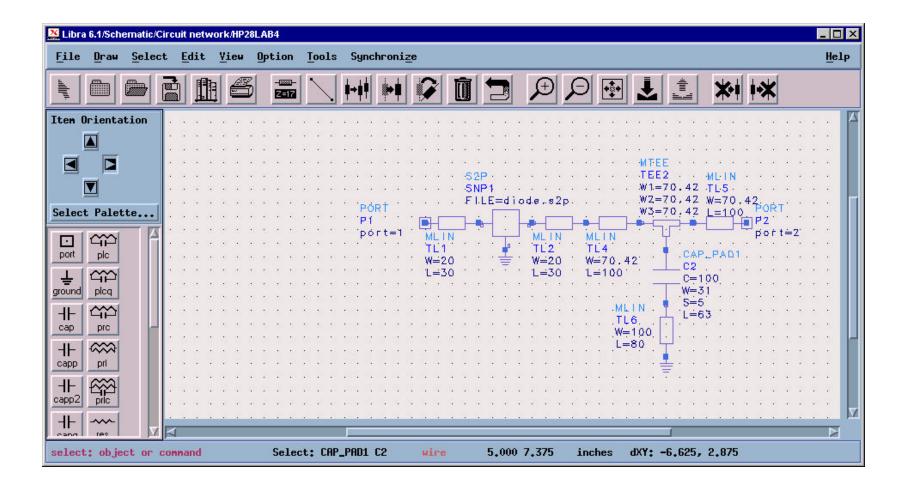
Define C, W,S,L from the data sheet. Units are from your default.

Capacitor (pad artwork) * CAP_PAD1 ID C2 Parameters	Yalue type Real Yalue 63]	Value Options	Units here are pF and mils
C=100 H=31 S=5 I=63			
OK Apply Canc	el Reset	Help	

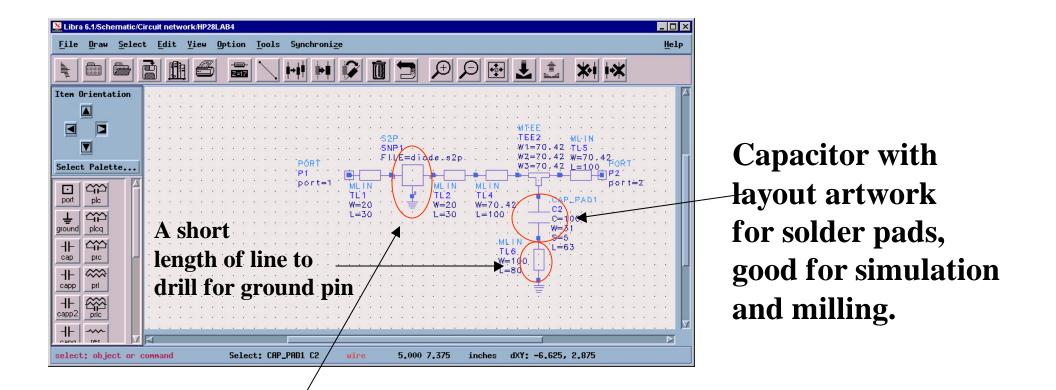
Put the capacitor on the circuit. Cap_pad1 is the artwork for the capacitor (pads that will be used for soldering and sufficient space between them.



Add components as needed: (Explanations follow)



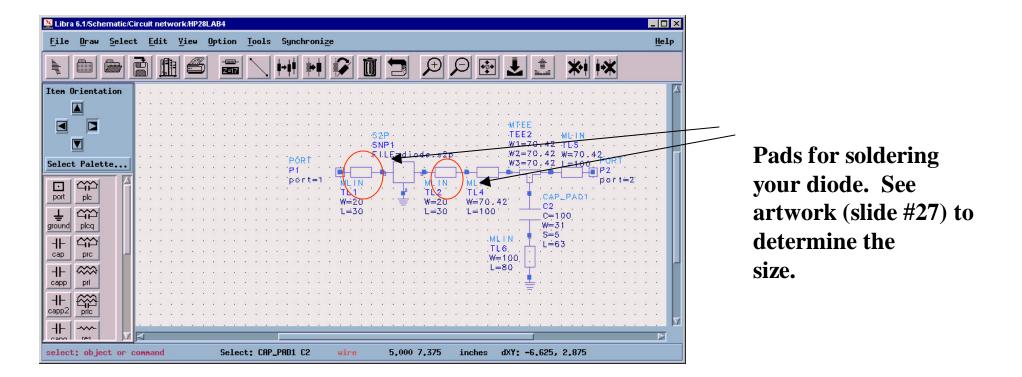
Diode and Capacitor



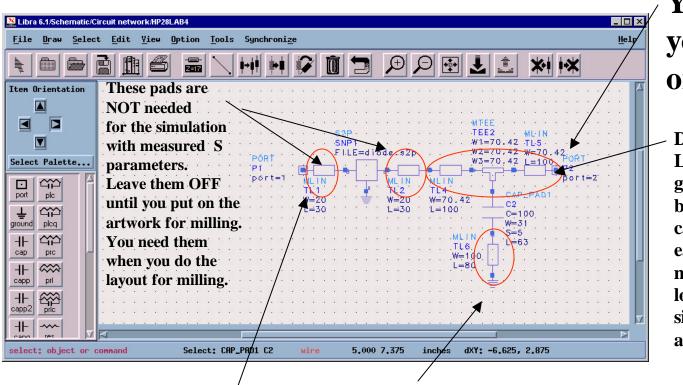
Your diode S-parameters for simulation. For milling, you will need artwork instead.

Diode Solder pads

IF you use diode S-parameters from the HP website, you will need to add the solder pads (which add length of lines). Find the size of solder pads in the data sheets OR the diode artwork. (See layout slide # 27 below.) Do NOT add these pads if you are using measured data, because they are already part of the measurements.



Physical connection of capacitor



You will attach your DC voltmeter on this port.

DC output line Long enough to get some separation between diode and cap (so they aren't on top of eachother). Doesn't matter much how long this is, as long as you account for it in the simulation, which is what you are doing here.

Remember a port

Ground pad (in Circuit Cam you will need to drill a hole for milling).

Now:

- Simulate the circuit in testbench and find Z_{11} for both frequencies (2.4 and 2.6 GHz)
- Use Tline or Smith Chart to design a stubmatching network. A short circuited stub is preferred.
- Follow the single-stub tutorial from here to add a stub matching network to the front of your circuit.
- Check your circuit (in testbench) to be sure your matching is good.

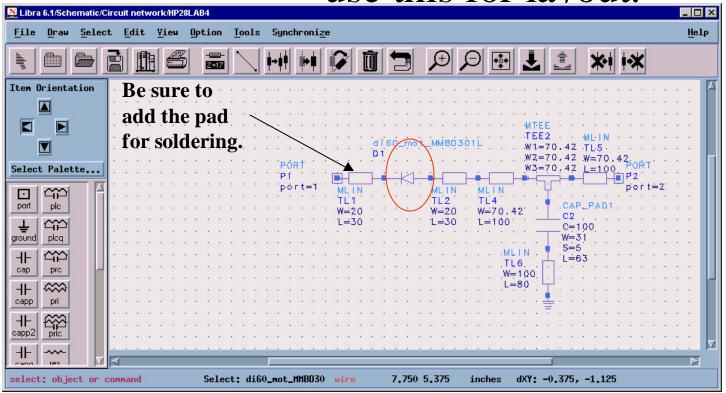
Layout:

Everything is OK for the layout except for your diode, which does not yet have proper artwork.

Choose any diode with an SOT23 package from the library

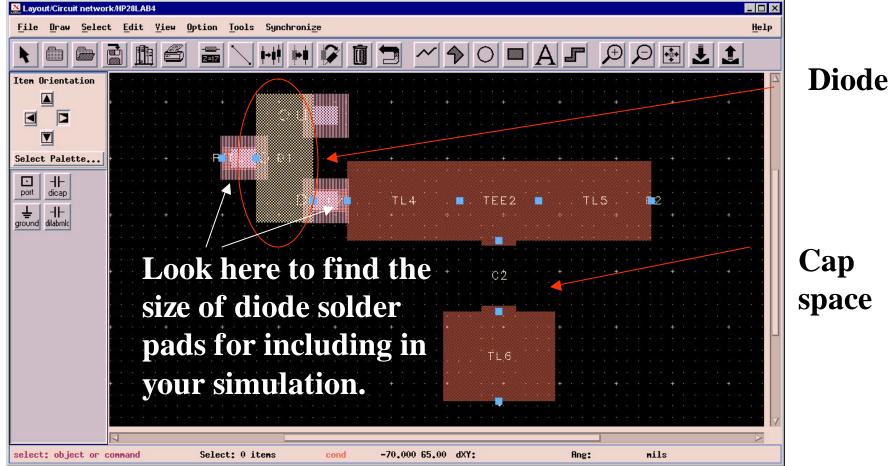
🔀 Library List	
Hitachi Diode Library v6.0 HP-Avantek Diode Library v6.0 Motorola Diode Library v6.0 Philips Diode Library v6.0 Siemens Diode Library v6.0 Sony Diode Library v6.0 Toshiba Diode Library v6.0 Bipolarics Chip BJT Library v6.0	di60_not_1N5139A (Motorola 1N5139A: 51-02 Package, If(nax)=250nA, Vr(nax)=60V, Pdiss=400 di60_not_1N5148 (Motorola 1N5148: 51-02 Package, If(nax)=250nA, Vr(nax)=60V, Pdiss=400nH di60_not_1N5444A (Motorola 1N5444A: 51-02 Package, If(nax)=NA, Vr(nax)=30V, Pdiss=400nH) di60_not_1N5455A (Motorola 1N5455A: 51-02 Package, If(nax)=NA, Vr(nax)=30V, Pdiss=400nH) di60_not_1N5456A (Motorola 1N5456A: 51-02 Package, If(nax)=NA, Vr(nax)=30V, Pdiss=400nH) <u>di60_not_8A199LT1 (Motorola 8A199LT1: S0T23 Package, If(nax)=50nA, Vr(nax)=70V, Pdiss=NA</u> di60_not_8AV70LT1 (Motorola 8AS16T: S0T23 Package, If(nax)=50nA, Vr(nax)=70V, Pdiss=NA di60_not_8AV70LT1 (Motorola 8AV70LT1: S0T23 Package, If(nax)=50nA, Vr(nax)=70V, Pdiss=NA
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Remove the S-parameter diode, and replace with the artwork diode. Don't simulate it, just use this for layout.

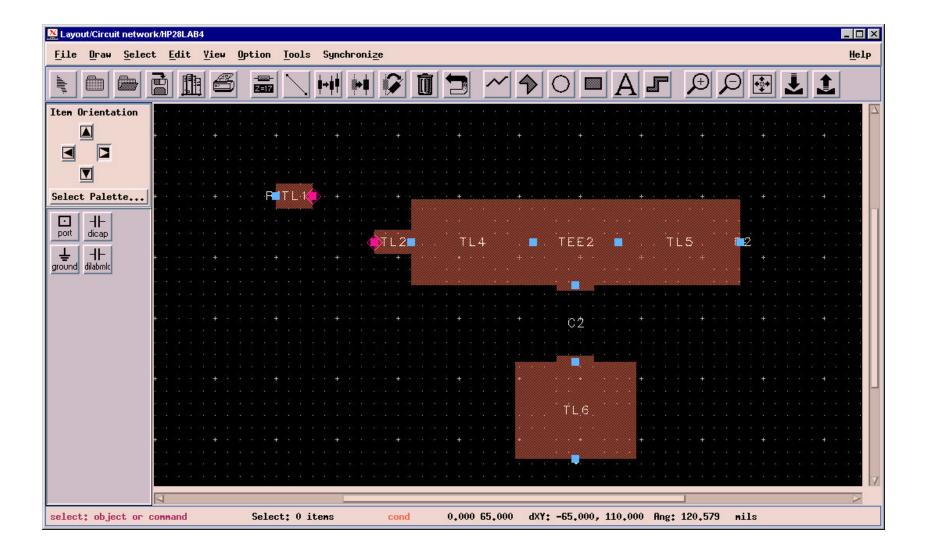


The diode you will use in your lab (HP 2850) is BACKWARDS from the standard package, so turn the artwork around.

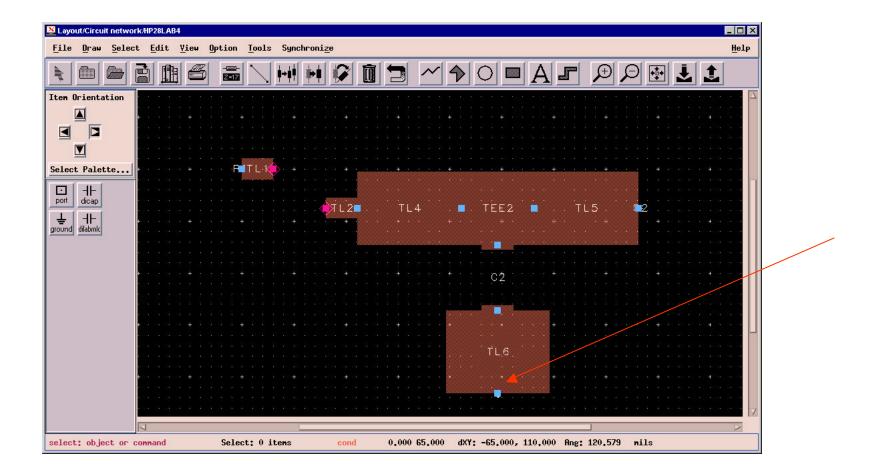
Synchronize, and take to layout. This is what it looks like without your matching circuit



Delete the diode from the picture to be milled, but remember what it looks like for soldering the diode.



Remember to put holes for all the shorts (ground, short ckt. stubs, etc.) when you mill the circuit



That's it! You are done!